

Application Number 09/975,628  
Responsive to Office Action mailed March 9, 2005

### AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application.

#### **Listing of Claims:**

**Claim 1 (Currently Amended):** A method comprising:

- storing data received from a host computer prior to a time  $T_0$  on a primary storage;
- storing data received from the host computer after time  $T_0$  on a secondary storage;
- defining a map designating corresponding logical storage units for the primary first and secondary storage;
- updating the map to indicate whether data has been written to the logical storage units of the secondary storage;
- receiving a read command issued by the host computer;
- identifying a logical storage unit designated by the read command;
- accessing the map to determine whether data has been written to the secondary storage within the identified logical storage unit; and
- selectively reading data from the primary storage and the secondary storage based on the determination.

**Claim 2 (Previously Presented):** The method of claim 1, further comprising storing the map in a computer-readable medium coupled to the host computer.

**Claim 3 (Previously Presented):** The method of claim 1, further comprising storing the map in a computer-readable medium of a controller coupled to the host computer via an input/output (I/O) bus.

**Claim 4 (Cancelled)**

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**Claim 5 (Previously Presented):** The method of claim 1, wherein the map comprises a bitmap having a set of binary values, wherein each binary value corresponds to a respective logical storage unit, and further wherein updating the map includes changing a state of at least one of the binary values.

**Claim 6 (Cancelled)**

**Claim 7 (Previously Presented):** The method of claim 1, further comprising:  
reading data from the secondary storage device when data has been written within the identified logical storage unit;  
reading data from the primary storage device when data has not been written within the identified logical storage unit; and  
communicating the read data to the host computer.

**Claim 8 (Previously Presented):** The method of claim 1, further comprising initializing the map in response to a restore signal.

**Claim 9 (Original):** The method of claim 5, further comprising setting the binary values of the bitmap to a first state in response to a restore command.

**Claim 10 (Original):** The method of claim 9, further comprising receiving the restore signal from a hardware switch.

**Claim 11 (Currently Amended):** The method of claim 1, comprising:  
receiving a reconcile command;  
copying data between the primaryfirst and secondary storage devices in response to the reconcile command until the primaryfirst and secondary storages store equivalent data; and  
resetting the map.

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Claim 12 (Original): The method of claim 1, further comprising:  
detecting free cycles for a bus in the host computer; and  
copying data from the primary storage to the secondary storage during the unused bus cycles.

Claim 13 (Previously Presented): The method of claim 12, further comprising  
receiving a reconcile command at a time  $T_R$ ;  
determining a remaining amount of data to copy from the primary storage to the secondary storage;  
copying the remaining data from the primary storage to the secondary storage when the remaining amount of data is less than the data received from the host computer after time  $T_0$ ; and  
resetting the map.

Claim 14 (Original): The method of claim 13, further comprising storing data received from the host computer after time  $T_R$  on the primary storage.

Claim 15 (Currently Amended): A method comprising:  
receiving a first reconcile command from a reconcile hardware switch at a time  $T_0$ ;  
establishing a  $T_0$  state in response to the reconcile command in which data received from the host computer prior to time  $T_0$  is stored on a primary storage and data received from the host computer after time  $T_0$  is stored on a secondary storage;  
receiving a restore command from a restore hardware switch subsequent to time  $T_0$ ; and  
disregarding the data received from the host computer after time  $T_0$  in response to the restore command.

Claim 16 (Cancelled)

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Claim 17 (Original): The method of claim 15, further comprising:

receiving a second reconcile command at a time subsequent to time  $T_0$ ; and  
establishing a new  $T_0$  state in response to receiving a second reconcile command.

Claim 18 (Currently Amended): The method of claim 15, further comprising:

defining a map designating corresponding logical storage units for the primary first and  
secondary storage; and  
updating the map to indicate whether data has been written to the logical storage units of  
the secondary storage.

Claim 19 (Previously Presented): A system comprising:

a host computer;  
a primary storage;  
a secondary storage;  
a controller to receive data from the host computer;  
a first switch to generate a reconcile command upon activation; and  
a second switch to generate a restore command upon activation,  
wherein the controller stores data received prior to the reconcile command on the primary  
storage, and data received after the reconcile command on the secondary storage, and  
wherein the controller discards the data stored on the secondary storage upon receiving  
the restore command.

Claim 20 (Cancelled)

Claim 21 (Previously Presented): The system of claim 19, wherein the first and second  
switches comprise hardware switches.

Claim 22 (Original): The system of claim 19, wherein the controller is coupled to the host  
computer via an input/output (I/O) bus.

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**Claim 23 (Original):** The system of claim 19, wherein the controller includes a computer-readable medium storing a record of the data written to the secondary storage, and wherein the controller selectively reads data from the primary storage device and the secondary storage device based on the record.

**Claim 24 (Currently Amended):** The system of claim 19, wherein the controller defines a map designating corresponding logical storage units for the primary first and secondary storage, and updates the map to indicate whether data has been written to the logical storage units of the secondary storage.

**Claim 25 (Previously Presented):** A system comprising:

- a connector adapted to provide an interface to a computer;
- primary storage connected to the connector, wherein the primary storage is adapted to store first data;
- secondary storage connected to the connector, wherein the secondary storage is adapted to store second data;
- a restoration switch; and
- a reconcile switch,

wherein the first data is data stored prior to time  $T_0$ ;

wherein the second data is data stored after  $T_0$ ;

wherein after time  $T_0$  and in response to a read command for the first data being received at the connector, the first data is sent from the primary storage to the computer;

wherein after time  $T_0$  and in response to a read command for the second data being received at the connector, the second data is sent from the secondary storage to the computer;

wherein actuating the restoration switch disregards the second data; and

wherein actuating the reconcile switch establishes a new  $T_0$ .

**Claim 26 (Original):** The storage device of claim 25, wherein the restoration switch includes a hardware switch.

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**Claim 27 (Original):** The storage device of claim 25, wherein the restoration switch includes a virtual switch implemented via software such that actuation of the restoration switch comprises receiving a command at the connector.

**Claim 28 (Cancelled)**

**Claim 29 (Original):** The storage device of claim 25, further comprising a controller for controlling the primary storage and the secondary storage, the controller including a map for tracking the second data received at the connection point and stored on the secondary storage.

**Claim 30 (Original):** The storage device of claim 25, wherein the reconcile switch includes a hardware switch.

**Claim 31 (Original):** The storage device of claim 25, wherein the reconcile switch includes a virtual switch implemented via software such that actuation of the reconcile switch comprises receiving a command at the connector.

**Claim 32 (Previously Presented):** The storage device of claim 25, wherein the new  $T_0$  is established by copying the second data from the secondary storage to the primary storage.

**Claim 33 (Previously Presented):** The storage device of claim 25, wherein the new  $T_0$  is established by copying the first data from the primary storage to the secondary storage during free bus cycles before the reconcile switch is actuated, and copying any of the first data from the primary storage to the secondary storage that has not already been copied to the secondary storage when the reconcile switch is actuated.

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Claim 34 (Previously Presented): A storage device for a computer, comprising:  
a connector adapted to provide an interface to the computer;  
a single computer-readable medium, including:  
primary storage connected to the connector, wherein the primary storage is adapted to store first data; and  
secondary storage connected to the connector, wherein the secondary storage is adapted to store second data; and  
a reconcile switch,  
wherein the first data is data stored prior to time  $T_0$ ;  
wherein the second data is data stored after  $T_0$ ;  
wherein after time  $T_0$  and in response to a read command for the first data being received at the connector, the first data is sent from the primary storage to the computer;  
wherein after time  $T_0$  and in response to a read command for the second data being received at the connector, the second data is sent from the secondary storage to the computer; and  
wherein actuating the reconcile switch establishes a new  $T_0$  state.

Claim 35 (Original): The storage device of claim 34, further comprising a restoration switch, wherein actuating the restoration switch places the secondary storage in a  $T_0$  state.

Claim 36 (Cancelled)

Claim 37 (Original): The storage device of claim 34, further comprising a controller for controlling the primary storage and the secondary storage, the controller including a map for tracking the second data received at the connection point and stored on the secondary storage.

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**Claim 38 (Previously Presented):** A method, comprising:

in response to receiving a read command regarding first data after time  $T_0$ , reading the first data from primary storage, wherein the first data is data stored prior to  $T_0$ ;

in response to receiving a write command after  $T_0$ , storing second data to secondary storage;

in response to receiving a read command regarding the second data, reading the second data from secondary storage; and

in response to a reconcile command, establishing a new  $T_0$ ; wherein establishing a new  $T_0$  includes establishing a new  $T_0$  in response to actuating a reconcile switch.

**Claim 39 (Cancelled)**

**Claim 40 (Original):** The method of claim 38, further comprising in response to actuating a restoration switch, disregarding the second data.

**Claim 41 (Original):** The method of claim 38, wherein establishing a new  $T_0$  includes copying second data from the secondary storage to the primary storage.

**Claim 42 (Original):** The method of claim 38, wherein establishing a new  $T_0$  state includes:

prior to actuating a reconcile switch, copying first data from the primary storage to the secondary storage during free cycles; and

in response to actuating a reconcile switch, copying any of the first data from the primary storage to the secondary storage that has not already been copied to the secondary storage.

**Claim 43 (Previously Presented):** An apparatus comprising:

a computer-readable medium with primary storage for storing first data and secondary storage for storing second data, wherein the first data is data stored prior to time  $T_0$  and the second data is data stored after  $T_0$ ;

a restoration switch for disregarding the second data; and

a reconcile switch for establishing a new  $T_0$ .



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Claim 44 (Original): The apparatus of claim 43, wherein the computer-readable medium comprises a fixed computer-readable medium.

Claim 45 (Original): The apparatus of claim 43, wherein the computer-readable medium comprises a single computer-readable medium.

Claim 46 (Original): The apparatus of claim 43, wherein the computer-readable medium includes no more than one computer-readable medium.

Claim 47 (Cancelled)

Claim 48 (Original): The apparatus of claim 43, further comprising a controller for storing the first data to the primary storage prior to time  $T_0$ , for storing the second data to the secondary storage after  $T_0$ , for reading the first data from primary storage in response to receiving a read command regarding the first data, for reading the second data from secondary storage in response to receiving a read command regarding the second data, and for establishing a new time  $T_0$  in response to an actuated reconcile switch.

Claim 49. (Currently Amended): A method comprising:  
storing data received from a host computer prior to a time  $T_0$  on a primary storage;  
storing data received from the host computer after time  $T_0$  on a secondary storage;  
maintaining a record of the data written to the secondary storage; and  
selectively reading data from the primary storage and the secondary storage based on the record;  
wherein the recordmap comprises a bitmap having a set of binary values,  
wherein each binary value corresponds to a respective logical storage unit, and wherein  
updating the recordmap includes changing a state of at least one of the binary values.

Claim 50. (Previously Presented): The method of claim 49, further comprising setting the binary values of the bitmap to a first state in response to a restore command.

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**Claim 51. (Previously Presented):** The method of claim 50, further comprising receiving the restore signal from a hardware switch.

**Claim 52. (Previously Presented):** A method comprising:  
receiving a first reconcile command from a reconcile hardware switch at a time  $T_0$ ;  
establishing a  $T_0$  state in response to the reconcile command in which data received from the host computer prior to time  $T_0$  is stored on a primary storage and data received from the host computer after time  $T_0$  is stored on a secondary storage;  
receiving a second reconcile command at a time subsequent to time  $T_0$ ; and  
establishing a new  $T_0$  state in response to receiving a second reconcile command.

**Claim 53. (Previously Presented):** A method comprising:  
in response to receiving a read command regarding first data after time  $T_0$ , reading the first data from primary storage, wherein the first data is data stored prior to  $T_0$ ;  
in response to receiving a write command after  $T_0$ , storing second data to secondary storage;  
in response to receiving a read command regarding the second data, reading the second data from secondary storage;  
in response to a reconcile command, establishing a new  $T_0$ ; and  
in response to actuating a restoration switch, disregarding the second data.